

2601

PTO/SB/21 (09-04)
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Total Number of Pages in This Submission

154

Application Number

10/006,072

Filing Date

12-05-2002

First Named Inventor

Clem

Art Unit

2661

Examiner Name

Washington

Attorney Docket Number

I000-P02159US

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
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<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Power of Attorney, Revocation	<input type="checkbox"/> Status Letter
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Remarks

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Firm Name	SoCal IP Law Group LLP		
Signature			
Printed name	Joel G. Landau		
Date	06-24-2005	Reg. No.	54,732

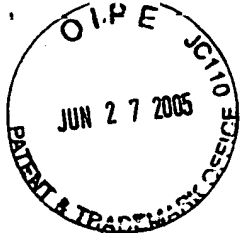
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/006,072 Confirmation No.
Applicant : Clem
Filed : 12-05-2002
TC/A.U. : 2661
Examiner : Washington
Docket No. : I000-P02159US
Customer No. : 33356

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Dear Sir:

General Matters

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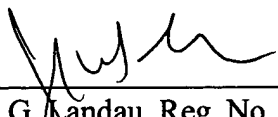
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Respectfully submitted,

Date: June 24, 2005



Joel G. Landau, Reg. No. 54,732

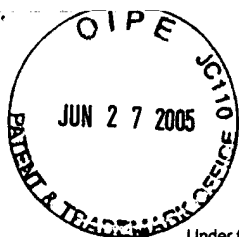
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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 3

Complete if Known

Application Number	10/006,072
Filing Date	12-05-2002
First Named Inventor	Clem
Art Unit	2661
Examiner Name	Washington
Attorney Docket Number	I000-P02159US

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 2002/0061022-A1	05-23-2002	Allen, Jr.	
		US- 2002/0099855-A1	07-25-2002	Bass	
		US- 5,784,003	07-21-1998	Dahlgren	
		US- 5,555,543	09-10-1996	Grohoski	
		US- 5,841,874	11-24-1998	Kempke	
		US- 5,898,689	04-18-1999	Kumar	
		US- 5,404,461	04-04-1995	Olnowich	
		US- 6,510,138-B1	01-21-2003	Pannell	
		US- 6,442,674-B1	08-27-2002	Lee	
		US- 6,574,194-B1	06-03-2003	Sun	
		US- 5,909,440	06-01-1999	Ferguson	
		US- 6,836,479-B1	12-28-2004	Sakamoto	
		US- 6,493,347-B2	12-10-2002	Sindhu	
		US- 5,550,823	08-27-1996	Irie	
		US- 6,611,527-B1	08-26-2003	Moriwaki	
		US- 6,067,408	05-23-2000	Runaldue	
		US- 6,172,927	01-09-2001	Taylor	
		US- 6,570,876-B1	05-27-2003	Aimoto	
		US- 6,735,219-B1	05-11-2004	Clauberg	

FOREIGN PATENT DOCUMENTS

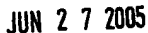
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Sheet 2

of 3

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Application Number	10/006,072
Filing Date	12-05-2002
First Named Inventor	Clem
Art Unit	2661
Examiner Name	Washington
Attorney Docket Number	I000-P02159US

U. S. PATENT DOCUMENTS

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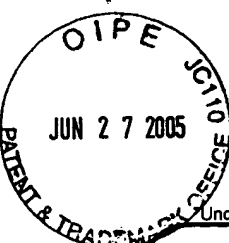
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		Application Number	10/006,072		
		Filing Date	12-05-2002		
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		Art Unit	2661		
		Examiner Name	Washington		
Sheet	3	of	3	Attorney Docket Number	I000-P02159US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		PETASWITCH SOLUTIONS, INC., The Pisces Chipset, Product Brief, 2001, http://www.peta-switch.com/products/product_brief.htm .	
		PETASWITCH SOLUTIONS, INC., PetaSwitch Solutions Announces Raising \$4 Million in First Round Financing, Press Release, 2001, http://peta-switch.com/newsroom/press_releases.htm .	
		PETASWITCH SOLUTIONS, INC., Company Overview, 2001, http://www.peta-switch.com/markets/overview.htm .	
		STILIADIS, ET AL., Rate-Proportional Servers: A Design Methodology for Fair Queueing Algorithms, December 1995, Computer Engineering & Information Sciences, University of California, Santa Cruz.	
		STOICA, ET AL., Earliest Eligible Virtual Deadline First: A Flexible and Accurate Mechanism for Proportional Share Resource Allocation, Department of Computer Science, Old Dominion University, Norfolk, VA.	
		GUPTA, Scheduling in Input Queued Switches: A Survey, June 1996, Department of Computer Science, Stanford University, California.	
		SCHOENEN, ET AL., Distributed Cell Scheduling Algorithms for Virtual-Output-Queued Switches, December 1999, pp. 1211-1215, Volume 1, GLOBECOM, IEEE Global Telecommunications Conference.	
		FAHMY, A Survey of ATM Switching Techniques, August 14, 2001, Department of Computer and Information Science, The Ohio State University.	

Examiner Signature	Date Considered
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